INFORMATION DISCLOSURE CITATION

Atty. Docket No.	04329.3125	Serial No.	10/049,940
Applicant	Katsuhiko OYAMA		
Filing Date	August 28, 2003	Group:	

	U.S. PATENT DOCUMENTS					
Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
CAL	6,271,056 B1	08/07/2001	Farnworth et al.			
						
		 				

FOREIGN PATENT DOCUMENTS						
	Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
CAL	2002-368185	12/20/2002	Japan			No
		, , , , , , , , , , , , , , , , , , , ,		ļ	ļ	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)			
Oyama et al.; "LAMINATED-CHIP SEMICONDUCTOR DEVICE"; U.S. Patent Application No. 10/156,819, filed May 30, 2002			

Examiner	Date Considered 3/17/05			
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.				
Form PTO 1449	atent and Trademark Office - U.S. Department of Commerce			